

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant: § Art Unit: 2482
Louis Lippincott et al. §
Serial No.: 10/750,075 § Examiner: Chikaodili E. Anyikire
Filed: December 31, 2003 § Conf. No.: 2083
For: Motion Estimation Sum of all § Docket: ITL.1703US
Differences (SAD) Array Having § P17498
Reduced Semiconductor Die § Assignee: Intel Corporation
Area Consumption §

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
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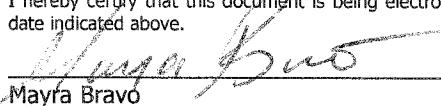
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/C.A./

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed January 19, 2011, please amend the above-referenced patent application as follows:

Date of Deposit: <u>March 8, 2011</u> I hereby certify that this document is being electronically transmitted on the date indicated above.
 Mayra Bravo